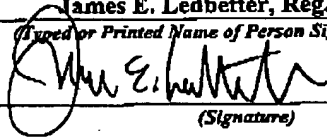


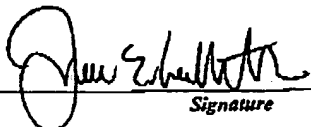
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STEVENS DAVIS

CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicant(s): Takeya FUJINO et al.			Docket No. L8462.03113
Application No. 10/685,387	Filing Date October 16, 2003	Examiner R. Potter	Group Art Unit 2822
Invention: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME			
<p>I hereby certify that this <u>Amendment under 37 CFR 1.111 w/ Amendment Transmittal</u> <i>(Identify type of correspondence)</i></p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u>)</p> <p>on <u>June 16, 2005</u> <i>(Date)</i></p> <div style="text-align: right; margin-top: 20px;"> <u>James E. Ledbetter, Reg. No. 28,732</u> <i>(Typed or Printed Name of Person Signing Certificate)</i>  <i>(Signature)</i> </div> <p style="text-align: center; margin-top: 40px;">Note: Each paper must have its own certificate of mailing.</p>			

P18/REV02

AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No. L8462.03113	
Applicant(s): Takeya FUJINO et al.					
Application No. 10/685,387	Filing Date October 16, 2003	Examiner R. Potter	Customer No.	Group Art Unit 2822	Confirmation No. 9778
Invention: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME					
COMMISSIONER FOR PATENTS:					
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	30 -	30 =	0 x	\$50.00	\$0.00
INDEP. CLAIMS	13 -	13 =	0 x	\$200.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
<div style="display: flex; justify-content: space-between;"><div><input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. <div style="margin-left: 20px;"><input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.</div></div><div style="text-align: right;">Dated: June 16, 2005</div></div> <div style="margin-top: 20px;"><div style="display: flex; justify-content: space-between;"><div style="width: 40%;"> _____ James E. Ledbetter Reg. No. 28,732</div><div style="width: 55%; border: 1px solid black; padding: 5px;"><div style="font-size: small;">I certify that this document and fee is being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div><div style="border-top: 1px solid black; margin-top: 10px; text-align: center;">Signature of Person Mailing Correspondence</div><div style="border-top: 1px solid black; margin-top: 10px; text-align: center;">Typed or Printed Name of Person Mailing Correspondence</div></div></div><div style="margin-top: 20px;">cc:</div></div>					

P11LAR35/REV07

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JUN 16 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventor: Takeya FUJINO et al. Group Art Unit: 2822
Appln. No.: 10/685,387 Examiner: R. Potter
Filed: October 16, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF
MANUFACTURING THE SAME

AMENDMENT UNDER 37 CFR § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 16, 2005,
please amend the above-captioned application as follows: